

In the Claims:

Please cancel claims 7-13, without prejudice, and amend claim 1 as follows:

1. (Currently Amended) A thin film transistor array substrate defined
by a plurality of pixel areas, comprising:

an insulating substrate;

a thin film transistor arranged in ~~a part of a pixel area~~ each of the pixel
areas on the insulating substrate;

an auxiliary capacitor having an auxiliary capacitor electrode arranged
in the pixel area and an opposite electrode facing the auxiliary capacitor electrode via an
insulating layer, the opposite electrode being located in the same layer as a source electrode
and a drain electrode of the thin film transistor; and

a pixel electrode formed in the pixel area, wherein the opposite electrode
is divided into two or more sections in each of the pixel areas, and each of the sections is
electrically connected to the pixel electrode via a contact hole.

2. (Original) The thin film transistor array substrate according to claim
1, wherein one of the divided sections of the opposite electrode is connected to the source
electrode of the thin film transistor via a connection located in the same layer as the opposite
electrode and the source electrode.

3. (Original) The thin film transistor array substrate according to claim 1, wherein the pixel electrode has a shape defining orientation of liquid crystal, and the electrical connection extends along a boundary between domains with different orientations of the liquid crystal.

4. (Original) The thin film transistor array substrate according to claim 2, wherein the source electrode, the connection, and said one of the divided sections of the opposite electrode are integrally formed of the same material.

5. (Original) The thin film transistor array substrate according to claim 3, wherein the pixel electrode has a shape with branches extending to the left and right, and the electrical connection extends along a center line of the pixel electrode.

6. (Original) A thin film transistor array substrate comprising:
an insulating substrate;
a thin film transistor arranged in a part of a pixel area on the insulating substrate;
an auxiliary capacitor having an auxiliary capacitor electrode arranged in the pixel area and an opposite electrode facing the auxiliary capacitor electrode via an insulating layer, the opposite electrode being located in the same layer as source and drain electrodes of the thin film transistor;

a connection located in the same layer as the source electrode and the opposite electrode and extending between the source electrode and the opposite electrode;
and

a pixel electrode formed in the pixel area, wherein the opposite electrode is electrically connected to the pixel electrode via a contact hole.

7-13. (Cancelled)